

(11) Publication number:

03292741 A

Generated Document.

PATENT ABSTRACTS OF JAPAN

(21) Application number: 02094610 (51) Intl. Cl.: H01L 21/336 H01L 21/20 H01L 21/84 H01L 29/784

(22) Application date: 10.04.90

(30) Priority:

(43) Date of application publication:

24.12.91

(84) Designated contracting

(71) Applicant: SEIKO EPSON CORP

(72) Inventor: TAKENAKA SATOSHI

(74) Representative:

(54) MANUFACTURE OF THIN FILM

SEMICONDUCTOR DEVICE

(57) Abstract:

PURPOSE: To form a silicon film consisting of a silicon crystal of a large crystal particle diameter and the interface between oxide films, which has a small interfacial level density, by a method wherein an amorphous semiconductor thin film is formed on an insulating substrate, is heat-treated without taking out in the atmosphere

to solid-phase grow and moreover, a gate oxide film is formed and the thin film and the oxide film are patterned into an insular form in one photo process.

a plasma CVD device, mixed gas substrate is installed in a chamber of exhausted, oxygen gas is introduced chamber is heated up to solid-phase (Si3H8) gas is introduced, an a-Si:H film 1-2 deposited by decomposition disilane (Si2H6) gas or trisilane containing monosilane (SiH4) gas, oxidation method using the plasma surface of the solid-phase grown Si and the gate oxide film and the solidoxidized to form a gate oxide film 1-4 and glow discharge is performed, grow the film 1-2 and after the gas is the temperature in the interior of the substituted for vacuum or inert gas, the gas is exhausted, the air is according to glow discharge and after CONSTITUTION: An insulating CVD device. film is oxidized by a plasma photo process. Subsequently, the end patterned into an insular form in one photolithography method and are phase grown Si film are etched by a whereby the surface of the film 1-2 is

COPYRIGHT: (C)1991, JPO& Japio

